Micro/Ib

Building: 0	RM:	MICRO # 0	Notes
Micro Number	Upstream Micro	Modem #	
DNS Micro Name	Downstream Micro	N.A.N. #	
IP Address	Backup Phone #	Port #	
Gateway Address	AC Panel & Breaker #	TTY #	
Hope / Mask: 255.255.252.0		Circuit #	

Power / Comm

Address	Description	Connector	To	Notes
	+12VDC	J8 1		
	GROUND	J8 2		
	+12VDC	J8 3		
	GROUND	J8 4		
	+12VDC	J8 5		
	GROUND	J8 6		
– 0 – 0	Micro & Power Supply Tamper Switches	J8 7		
– 0 – 1	AC Power Fail and Power Supply Supervision	J8 8		Power supply is on internal batteries, or there are battery problems.

8RP Board #1

	Address	Bldg	Description	Rm	Connector	To	Reader Type	Notes	REX	DO	DI
1	– 1 – 0				J2 1–4				1–8	1–0	1–0
2	– 1 – 1				J2 5–8				1–9	1–1	1–1
3	– 2 – 0				J3 1–4				2–8	2–0	2–0
4	– 2 – 1				J3 5–8				2–9	2–1	2–1
5	– 3 – 0				J4 1–4				3–8	3–0	3–0
6	– 3 – 1				J4 5–8				3–9	3–1	3–1
7	– 4 – 0				J5 1–4				4–8	4–0	4–0
8	– 4 – 1				J5 5–8				4–9	4–1	4–1

8RP Board #2

	Address	Bldg	Description	Rm	Connector	To	Reader Type	Notes	REX	DO	DI
9	– 5 – 0				J2 1–4				5–8	5–0	5–0
10	–5 – 1				J2 5–8				5–9	5–1	5–1
11	– 6 – 0				J3 1–4				6–8	6–0	6–0
12	– 6 – 1				J3 5–8				6–9	6–1	6–1
13	– 7 – 0				J4 1–4				7–8	7–0	7–0
14	– 7 – 1				J4 5–8				7–9	7–1	7–1
15	– 8 – 0				J5 1–4				8–8	8–0	8–0
16	– 8 – 1				J5 5–8				8–9	8–1	8–1

20 IO Board #1

	Address	Bldg	Description	Rm	Connector	From	Notes
1	– 1 – 16				J2 1–2		
2	– 1 – 17				J2 3–4		
3	– 1 – 18				J2 5–6		
4	– 1 – 19				J2 7–8		
5	– 1 – 20				J2 9–10		
6	– 1 – 21				J3 1–2		
7	– 1 – 22				J3 3–4		
8	– 1 – 23				J3 5–6		
9	– 1 – 24				J3 7–8		
10	– 1 – 25				J3 9–10		
11	– 1 – 26				J4 1–2		
12	– 1 – 27				J4 3–4		
13	– 1 – 28				J4 5–6		
14	– 1 – 29				J4 7–8		
15	– 1 – 30				J4 9–10		
16	– 1 – 31				J5 1–2		
17	– 1 – 32				J5 3–4		
18	– 1 – 33				J5 5–6		
19	– 1 – 34				J5 7–8		
20	– 1 – 35				J5 9–10		

16 DOR Board #1

	Address	Bldg	Description	Rm	Connector	From	Notes
1	– 1 – 16				J2 1–3		
2	– 1 – 17				J2 4–6		
3	– 1 – 18				J2 7–8		
4	– 1 – 19				J2 9–10		
5	– 1 – 20				J3 1–3		
6	– 1 – 21				J3 4–6		
7	– 1 – 22				J3 7–8		
8	– 1 – 23				J3 9–10		
9	– 1 – 24				J4 1–3		
10	– 1 – 25				J4 4–6		
11	– 1 – 26				J4 7–8		
12	– 1 – 27				J4 9–10		
13	– 1 – 28				J5 1–3		
14	– 1 – 29				J5 4–6		
15	– 1 – 30				J5 7–8		
16	– 1 – 31				J5 9–10		

16 DOR Board #2

	Address	Bldg	Description	Rm	Connector	To	Notes
1	– 2 – 16				J2 1–3		
2	– 2 – 17				J2 4–6		
3	– 2 – 18				J2 7–8		
4	– 2 – 19				J2 9–10		
5	– 2 – 20				J3 1–3		
6	– 2 – 21				J3 4–6		
7	– 2 – 22				J3 7–8		
8	– 2 – 23				J3 9–10		
9	– 2 – 24				J4 1–3		
10	– 2 – 25				J4 4–6		
11	– 2 – 26				J4 7–8		
12	– 2 – 27				J4 9–10		
13	– 2 – 28				J5 1–3		
14	– 2 – 29				J5 4–6		
15	– 2 – 30				J5 7–8		
16	– 2 – 31				J5 9–10		

LRB #1

	Addr	Bldg	Description	Input From	To	Rm
1	– 1 – 0			8RP1 J2–4		
2	– 1 – 1			8RP1 J2–8		
3	– 2 – 0			8RP1 J3–4		
4	– 2 – 1			8RP1 J3–8		
5	– 3 – 0			8RP1 J4–4		
6	– 3 – 1			8RP1 J4–8		
7	– 4 – 0			8RP1 J5–4		
8	– 4 – 1			8RP1 J5–8		

LRB #2

	Addr	Bldg	Description	Input From	To	Rm
1	– 5 – 0			8RP2 J2–4		
2	–5 – 1			8RP2 J2–8		
3	– 6 – 0			8RP2 J3–4		
4	– 6 – 1			8RP2 J3–8		
5	– 7 – 0			8RP2 J4–4		
6	– 7 – 1			8RP2 J4–8		
7	– 8 – 0			8RP2 J5–4		
8	– 8 – 1			8RP2 J5–8		

Fuse Board #1

	Type	Size	Description	To	Connector	Rm
1	3AG S–B	5A				
2	3AG S–B	5A				
3	3AG S–B	5A				
4	3AG S–B	5A				
5	3AG S–B	5A				
6	3AG S–B	5A				
7	3AG S–B	5A				
8	3AG S–B	5A				

Fuse Board #2

	Type	Size	Description	To	Connector	Rm
1	3AG S–B	5A				
2	3AG S–B	5A				
3	3AG S–B	5A				
4	3AG S–B	5A				
5	3AG S–B	5A				
6	3AG S–B	5A				
7	3AG S–B	5A				
8	3AG S–B	5A				

Fuse Board #3

	Type	Size	Description	To	Connector	Rm
1						
2						
3						
4						
5						
6						
7						
8						

MICRO–5 LOADING SCHEDULE

SCALE: NONE

1

LBNL ELECTRICAL STANDARDS
SECURITY CARD ACCESS
MICRO–5 LOADING SCHEDULE

DRAWN BY	DATE
CHECKED BY	
APPROVED BY	
CAD FILE PATH	
SCALE NTS	
DRAWING NO. SEC152	SHEET
PROJECT NO.	

UNIVERSITY OF CALIFORNIA LAWRENCE BERKELEY NATIONAL LABORATORY
FACILITIES DIVISION

T:\groups\ELECT\Romy\STD\Security Card\SEC152 MICRO–5 LOADING SCHEDULE.dwg